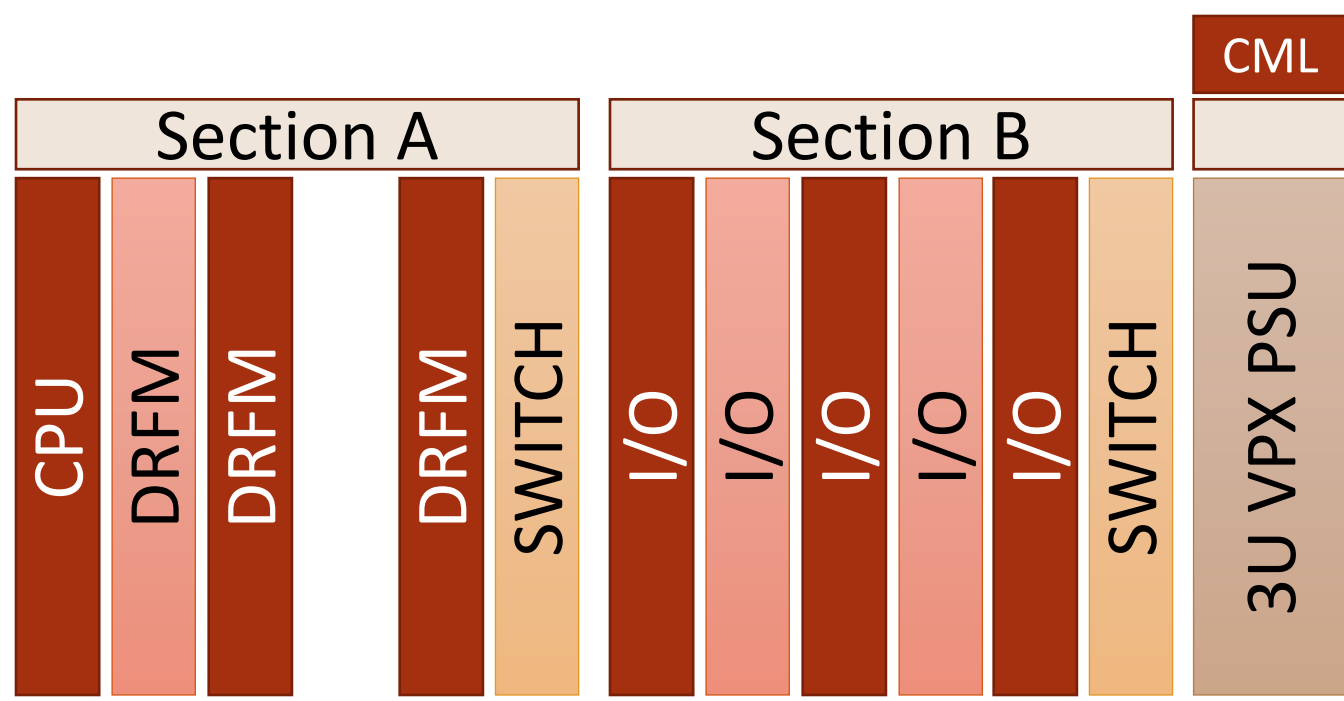


Target Generator and DRFM Solutions

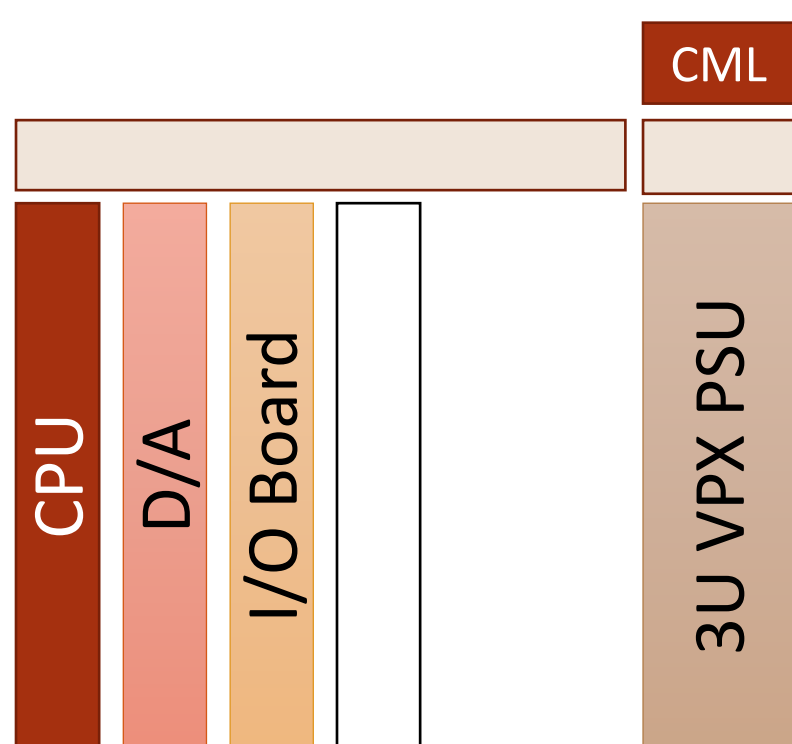


System 1



System 2

Architecture	3U, 19" rack-mountable chassis - OpenVPX architecture	
General application	Very low latency transceiver for DRFM application / target generation based on latest Intel-based SBC for control and communication	
Configuration	<ul style="list-style-type: none"> sampling rate up to 5.4GHz instantaneous bandwidth up to 1 GHz based on TI ADC12DL3200 and DAC12DL3200 chipset based on latest AMD FPGA VU13P for real-time signal processing 	<ul style="list-style-type: none"> sampling rate up to 64GHz instantaneous bandwidth up to 1 GHz based on Altera Agilex-9 DirectRF chip
Functionality	multi target delay (up to several ms, independently for each target) and doppler frequency shift coherent and non-coherent scenarios programmable white noise generator and jamming	



System 1



System 2

System 3

Architecture	3U, 19" rack-mountable chassis – OpenVPX or SOSA-aligned architecture		
General application	ultra wide-band generator with real-time programmable scenarios, ultra-fast frequency hopping and real-time recording based on latest Intel-based SBC for control and communication		
Configuration	<ul style="list-style-type: none"> sampling rate up to 64Gbps instantaneous bandwidth up to 2 GHz 2x ADC + 2x DAC based on Jariet Electra-MA transceiver based on latest AMD FPGA VU13P for real-time signal processing 	<ul style="list-style-type: none"> sampling rate up to 64GHz instantaneous bandwidth up to 2 GHz 8x ADC + 8x DAC based on Altera Agilex-9 DirectRF chip 	<ul style="list-style-type: none"> sampling rate up to 4GHz instantaneous bandwidth up to 2 GHz 4x ADC + 4x DAC based on AMD Gen. 3 RFSoc
Functionality	Target Generator	SIGINT and EW	DRFM

Defence Systems Applications

DRFM

Signal Generator

HWIL Test Equipment

Simulator

Production Test Bench

