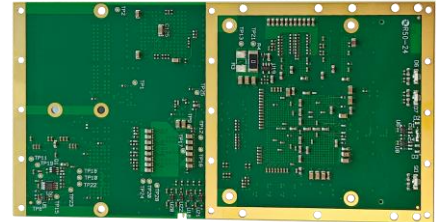


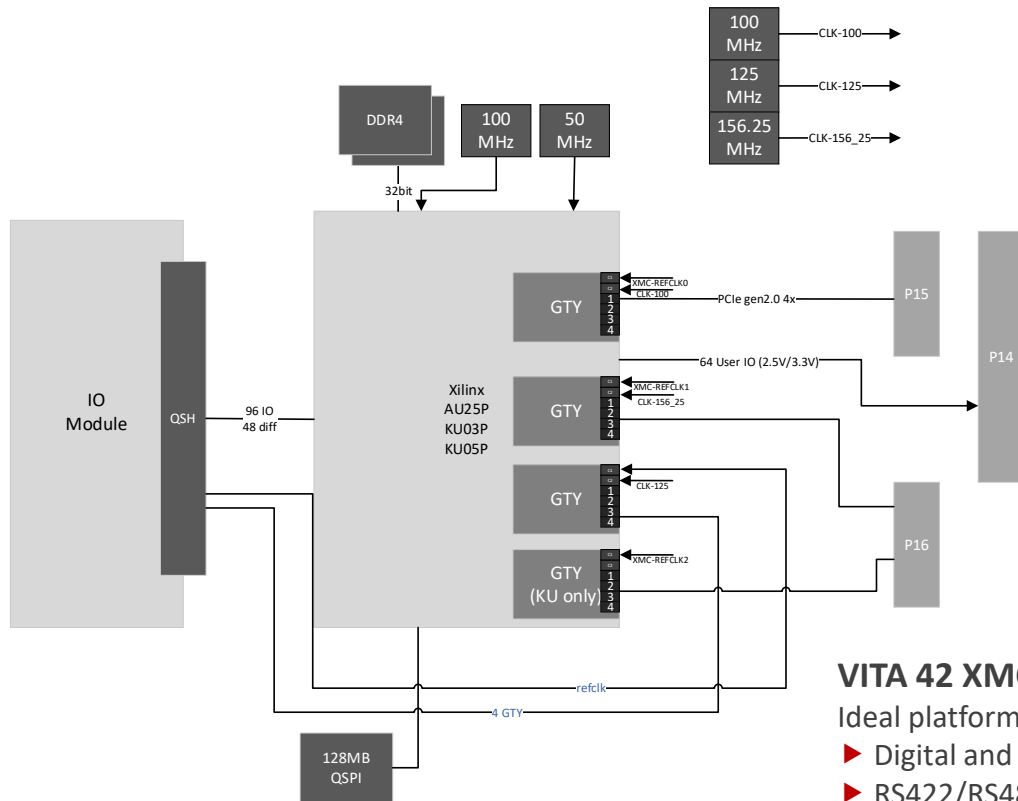
LXMC-X101 XMC

XMC FPGA Board with I/O



Key Features

LXMC-X101 is a Xilinx Ultrascale+ (Artix or Kintex) XMC board with pluggable frontend for a wide variety of I/O and high speed serial communication purpose.



VITA 42 XMC FPGA board

Ideal platform for:

- ▶ Digital and Analog I/O
- ▶ RS422/RS485 serial bus handling
- ▶ 10Gbit Ethernet solutions
- ▶ Low demanding DSP processing



Specification

Characteristics

- ▶ XMC FPGA Board
- ▶ FPGAs
 - XCAU25P
 - XCKU3P
 - XCKU5P
- ▶ 2GB 32bit DDR4 memory
- ▶ I/O slot

Environmental

- ▶ Commercial operating temperature (N-grade): 0°C to +55°C, air cooled
- ▶ Extended operating temperature (E-grade): -25°C to +70°C, air cooled
- ▶ Industrial operating temperature (K-grade): -40°C to +70°C, air cooled
- ▶ Ruggedized Conduction cooled (RC-grade): -40°C to +85 °C—no front panel

Power consumption

- ▶ 5 - 15 W (user FW dependent, excluding I/O)

Mechanical

- ▶ VITA 42

Firmware (available through the LVD FPGA FDK – to be ordered separately)

- ▶ PCIe end-point
- ▶ 4 channel DMA engine
- ▶ Example design
- ▶ Full source code

Software (available through the LVD FPGA SDK – to be ordered separately)

- ▶ Linux driver
- ▶ RTX driver (available on demand)
- ▶ Full source code

Certification

- ▶ REACH / RoHS compliant

Ordering information

Base Part Number: LXMC-X101-aaaa-vv-t

Variant (-aaaa)	FPGA
LXMC-X101-AU25-vv-t	XCAU25P-1SFVB784
LXMC-X101-KU3P-vv-t	XCKU3P-1SFVB784
LXMC-X101-KU5P-vv-t	XCKU5P-1SFVB784

Variant (-vv)	FPGA
LXMC-X101-aaaa-25-t	2.5V P14 I/O
LXMC-X101-aaaa-33-t	3.3V P14 I/O

Variant (-t)	Grade
LXMC-X101-aaaa-vv	N-grade
LXMC-X101-aaaa-vv-E	E-grade
LXMC-X101-aaaa-vv-K	K-grade
LXMC-X101-aaaa-vv-RC	Ruggedized Conduction Cooled

Popular Example: LXMC-X101-AU25-25

XCAU25P FPGA, with 2.5V P14 I/O, commercial temperature grade

Related products

LVD-FDK/X101-xy	LXMC-X101 firmware development kit
LVD-SDK/X101-xy	LXMC-X101 software development kit

Please contact directly LVD Systems for any other information needed for this product

